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END 000010 US1
Amendment dated 01/28/2004

09/535,069
Reply to office action mailed 10/28/2003

Amendments to the Specification:

Please replace the paragraph beginning at page 12, line 6, with the following rewritten paragraph:

B¹

It should be understood that the processing burden of control of the memory controllers is sufficiently light that the dedicated processor can be used without compromise of other desired functions and such use may provide simplification of some other functions such as scanning the program content for particular locations, as discussed in concurrently filed U. S. Patent application application S. N. 09/____,____, 09/534,643 (Docket No. END000009US1) assigned to the assignee of the present invention and hereby fully incorporated by reference. Alternatively, an additional processor can be provided at relatively little expense since processing requirements for control of the memory controller 135 is minimal to provide encryption without modification of the full granularity of the data in accordance with the invention.

Please replace the paragraph beginning at page 14, line 13, with the following rewritten paragraph:

B²

Next, entries in a local copy of the FAT in memory 130 are allocated for data blocks to be written to mass storage 150 at the sector location indicated in the header of each data block as shown at step 340. Non-sequential entries may be selected based on key 2 330' which may be and preferably is the same as key 1. The data blocks are then written to the sectors indicated in their respective headers (step 345) with the storage of each block being followed by incrementing of the counter (step 350), updating of the FAT in memory and repeating steps 340, 345 and 350 for each data block until the counter indicates that all data blocks in the memory queue have

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B²
been stored to mass storage 150 (step 360), looping as indicated at 365. Then, as depicted at step 360, the FAT is encrypted with key 3 330" which, again, is preferably the same as key 1. The encrypted FAT is then written to mass storage 150, the next memory queue is called as indicated at steps 380 and 390, respectively and the process is repeated, looping to step 310.
